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12. The microcontroller network of Claim 11, wherein a selected one of the microcontrollers checks for a microcontroller bus time-out.

13. The microcontroller network of Claim 11, wherein a selected one of the microcontrollers checks for a manual system board reset.

14. The microcontroller network of Claim 11, wherein a selected one of the microcontrollers checks for a software reset command.

15. The microcontroller network of Claim 11, wherein a selected one of the microcontrollers checks for system faults.

16. The microcontroller network of Claim 11, wherein a selected one of the microcontrollers maintains a system log in a non-volatile random access memory.

17. The microcontroller network of Claim 11, wherein a selected one of the microcontrollers monitors the speed of a canister fan.

18. The microcontroller network of Claim 11, wherein the processor bus is an I²C bus.

19. A system for monitoring and diagnosing a computer having a ~~microcontroller~~ network, the system comprising:

means for sensing the conditions of the computer with the microcontroller network;

means for receiving the sensed conditions in the microcontroller network; and

means for communicating the sensed conditions from the microcontroller network
to the system bus of the computer.

REMARKS

By this paper, Claims 11-19 have been added. Favorable action on the merits is earnestly solicited. Should there by any issues which can be resolved by telephone, the Examiner is